

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Devins et al.

Art Unit: 2128

Application No.: 09/683,677

Examiner: ALHIJA, SAIF A.

Filed: 02/01/2002

Dkt. No.: BUR920010098US1

Title: **METHOD OF SWITCHING EXTERNAL MODELS IN AN AUTOMATED
SYSTEM-ON-CHIP INTEGRATED CIRCUIT DESIGN VERIFICATION
SYSTEM**

Honorable Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

OFFICE ACTION RESPONSE

Sir:

This communication is in response to the Office Action mailed February 26, 2007.